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| APPLICATION | FILING or   | GRP ART |               |                |            |            |
|-------------|-------------|---------|---------------|----------------|------------|------------|
| NUMBER      | 371(c) DATE | UNIT    | FIL FEE REC'D | ATTY.DOCKET.NO | TOT CLAIMS | IND CLAIMS |
| 10/595 157  | 03/09/2006  | 1794    | 900           | 2006-0223A     | 14         | 2          |

**CONFIRMATION NO. 3496** 

52349 WENDEROTH, LIND & PONACK L.L.P. 2033 K. STREET, NW SUITE 800 WASHINGTON, DC 20006

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FILING RECEIPT

Date Mailed: 07/07/2008

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### Applicant(s)

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## **Assignment For Published Patent Application**

MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD., Osaka, JP

Power of Attorney: The patent practitioners associated with Customer Number <u>52349</u>

#### Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP2005/018109 09/30/2005

## **Foreign Applications**

JAPAN 2004/295923 10/08/2004

#### If Required, Foreign Filing License Granted: 07/02/2008

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US 10/595,157** 

**Projected Publication Date:** 10/16/2008

Non-Publication Request: No

Early Publication Request: No

Title

Method of Manufacturing Multi-Layer Circuit Board

# **Preliminary Class**

428

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